



### SIVA MANGAI.N.M

Associate Professor, Department of Electronics and Communication Engineering

Karunya Institute of Technology and Sciences

Coimbatore 641114, Tamil Nadu, India

Mobile : +91 9944181844

Email : [sivamangai@karunya.edu](mailto:sivamangai@karunya.edu) / [nmsivam@gmail.com](mailto:nmsivam@gmail.com)

### RESEARCH/TEACHING INTERESTS

Hardware Security, Design and Testing of Semiconductor Memories, Quantum Circuit Design, Hardware design verification techniques using System Verilog, Low Power VLSI Design, FPGA and ASIC Design, Digital system design, Analysis and design of Digital IC, Testing of VLSI circuits and Entrepreneurship in Technology.

### EXPERIENCE

- Associate Professor, Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, India, 2012 – till date
- Assistant Professor, Electronics and Communication Engineering, PSG College of Technology, India, 2010 – 2012
- Lecturer, Electronics and Communication Engineering, PSG College of Technology, India, 2006 – 2010
- Lecturer, Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, Coimbatore, India, 2003-2006.

### EDUCATION

Degree	Branch / Specialization	University	Class	Mode	Month & Year of Passing
Ph.D.,	Information and Communication Engineering	Anna University, Chennai	-	part Time	December 2011
M.E.,	VLSI Design	PSG College of Technology, Bharathiar University	First Class with Distinction	Full Time	March 2002
B.E.,	Electronics and Communication	PSNA College of Engineering & Technology, Madurai Kamaraj University	First Class with Distinction	Full Time	April 2000

### SUMMARY OF INTERNATIONAL JOURNAL & CONFERENCE PUBLICATIONS - [PUBLISHED & ACCEPTED]

Year	International Journal Publications	International Conference Publications	National Conference publications	Book Chapters
2007 - 2025	32	43	6	3

### PATENTS

1	Graphene Optical Data Storage using Nanoholes, Patent No. 449260, Granted Date: 04/09/2023
---	--

### UNIVERSITIES VISITED ABROAD

S.No	Name of the University	Country	Year
1	Ohio University	USA	2018
2	University of Peradeniya	Sri Lanka	2007

### RESEARCH GUIDANCE

Research guidance as supervisor	Completed	Ongoing
Doctor of Philosophy - PhD Thesis advising	8	3
Master of Engineering - Master's Thesis advising	39	1

### PROFESSIONAL MEMBERSHIPS

1	Senior member of IEEE, member of VLSI Society of India (VSI), member of International Association of Engineers.
---	---

### AWARDS/HONORS

- Convener - IEEE International Conferences ICDCS'24, ICDCS'22, ICDCS'20, and ICDCS'18
- Woman of Excellence Award – Indian Achievers Forum (2020)
- Outstanding Woman Achiever (Engineering) in VLSI Design – VIWA Foundation (2016)
- IETE Students' Journal Award (2014)