

**MANOJ.G**

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 Karunya Institute of Technology and Sciences
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RESEARCH/TEACHING INTERESTS

Analysis and Design of Analog Integrated Circuit, VLSI Design, Microprocessors, Microcontrollers, Linear Integrated Circuits, Electronic circuits, Computer Communication, Communication Engineering, Digital Electronics, Digital Communication, Electron Devices.

EXPERIENCE

Assistant Professor, Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, India, 2010- 2021

Lecturer, Electronics and Communication Engineering, Kumaraguru College of Engineering and Technology, India, 2007 - 2009

Lecturer, Electronics and Communication Engineering, Erode Sengunthar College of Engineering and Technology and Sciences, Coimbatore, India, 2006 - 2007

EDUCATION

Degree	Branch / Specialization	University	Class	Mode	Month&Year of Passing
Ph.D.	Electronics and Communication	Anna University	-	Part Time	April 2016
M.E.	VLSI DESIGN	Karunya Deemed University	Distinction	Full Time	March 2006
B.E.	Electronics and Communication	Bharathiar University	First Class	Full Time	April 2003

SUMMARY OF INTERNATIONAL JOURNAL& CONFERENCE PUBLICATIONS - [PUBLISHED & ACCEPTED]

Year	International Journal Publications	International Conference Publications	National Conference publications
2009 - 2021	20	11	2

RESEARCH PROJECTS

#	Title of the project	Funding Agency	Amount(Rs.)	Year
1	Development of Electronic Speed Controller for Actuator with a Feedback Mechanism	DRDO	9.4 lakhs	2020-2021
2.	Low Power Techniques for Biomedical Implantable Devices	Ministry of Electronics and Information Technology(MeitY),Government of India, through Visvesvarya Scheme for Electronics & IT	2.5 Lakhs	2019-2022
3.	Digitalization Technology in Rural Indias	Unnath Bharth Abhiyan, GoI	0.5 Lakhs	2018-2019

RESEARCH GUIDANCE

Research guidance as supervisor	Completed	Ongoing
Doctor of Philosophy - PhD Thesis advising	-	6
Master of Engineering - Master's Thesis advising	10	-

PROFESSIONAL MEMBERSHIPS

1	Life member of National Service scheme
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