

**D.S.SHYLU SAM**

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 Karunya Institute of Technology and Sciences
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RESEARCH/TEACHING INTERESTS

Analog VLSI Design, Analysis and Design of Analog Integrated Circuits, CAD for VLSI, ASIC Design, Digital Electronics, Digital Integrated Circuit Design, Linear Integrated Circuits, CMOS VLSI Design, Embedded Systems, High Speed VLSI Design, Mixed mode modeling, Photonics, Testing and Testability, SoC Design, VLSI Design, VLSI Sub System Design, Hardware Verification Techniques, Verilog HDL

EXPERIENCE

Associate Professor, Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, Coimbatore, India, 2017 - present

Assistant Professor (SG), Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, India, 2009 - 2017.

Assistant Professor, Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, India, 2009 .

Sr.Lecturer, Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, India, 2007 - 2008.

Lecturer, Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, Coimbatore, India, 2005 - 2007.

Lecturer, Electrical and Electronics Engineering, Noorul Islam College of Engineering , India, 2003 - 2005.

Lecturer, Electronics and Communication Engineering, The Rajaas Engineering College , India, 2003 .

EDUCATION

Degree	Branch / Specialization	University	Class	Mode	Month&Year of Passing
Ph.D.,	Electronics and Communication Engineering	Karunya University		Part Time	Dec.2016
M.Tech.,	VLSI Design	SASTRA University	Distinction	Full Time	Dec. 2002
B.E.,	Electrical and Electronics Engineering	M.S. University	First Class	Full Time	Apr.2001

SUMMARY OF INTERNATIONAL JOURNAL& CONFERENCE PUBLICATIONS - [PUBLISHED & ACCEPTED]

Year	International Journal Publications	International Conference Publications	National Conference publications
2005 - 2021	26	19	19

RESEARCH PROJECTS

#	Title of the project	Funding Agency	Amount(Rs.)	Year
1	Development and Implementation of a wearable smart security system for women using SoC	SERB	37.31 lakhs	Under Evaluation

RESEARCH GUIDANCE

Research guidance as supervisor	Completed	Ongoing
Doctor of Philosophy - PhD Thesis advising	-	2
Master of Engineering - Master's Thesis advising	28	3

PROFESSIONAL MEMBERSHIPS

1	Senior member of IEEE
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AWARDS/HONORS

1. Best Poster Award-Sep.2017