

**SIVA MANGAI .N.M**

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RESEARCH/TEACHING INTERESTS

Design of Nano scale semiconductor memories, Semiconductor Memory testing, optimization techniques, Design of Fault and Process parameter variation tolerant VLSI circuits, Design VLSI architectures that specifically address the social issues, Digital Electronics, Analysis and Design of Digital IC, Digital System design using VHDL, VLSI Design, ASIC Design, Semiconductor memory design and testing, Hardware Verification Techniques, CMOS VLSI Design, High speed VLSI Design, Semiconductor devices and modeling, Advanced Digital system design and testing, Low power VLSI design, CAD for VLSI, Testing of VLSI circuits, Advanced Semiconductor memories.

EXPERIENCE

Associate Professor, Department of Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, Coimbatore, India, 2012 - present

Assistant Professor, Department of Electronics and Communication Engineering, PSG College of Technology, India, 2010 - 2012

Lecturer, Department of Electronics and Communication Engineering, PSG College of Technology, India, 2006 - 2010

Lecturer, Department of Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, India, 2003 - 2006

EDUCATION

Degree	Branch / Specialization	University	Class	Mode	Month & Year of Passing
Ph.D.,	Information and Communication Engineering	Anna University, Chennai	-	part Time	Dec 2011
M.E.,	VLSI Design	Bharathiar University	First Class with Distinction	Full Time	March 2002
B.E.,	Electronics and Communication	Madurai kamaraj University	First Class with Distinction	Full Time	April 2000

SUMMARY OF INTERNATIONAL JOURNAL & CONFERENCE PUBLICATIONS - [PUBLISHED & ACCEPTED]

Year	International Journal Publications	International Conference Publications	National Journal /Conference publications
2006 - 2021	34	41	9

PATENTS

1	Graphene optical data storage using nanoholes, Application No. 201941026006, Publication Date: 05/07/2019
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UNIVERSITIES VISITED ABROAD

S.No.	Name of the University	Country	Year
1	Ohio University	USA	2018
2	University of Peradeniya	Sri Lanka	2007

RESEARCH GUIDANCE

Research guidance as supervisor	Completed	Ongoing
Doctor of Philosophy - PhD Thesis advising	4	6
Master of Engineering - Master's Thesis advising	32	3

PROFESSIONAL MEMBERSHIPS

Senior member of IEEE, member of VLSI Society of India (VSI), member of International Association of Engineers.

AWARDS/HONORS

1. IETE-Students' Journal Awardee 2014
2. Received outstanding women achiever (Engineering) in VLSI Design 2016 from VIWA foundation.
3. Received Woman of Excellence Award 2020 from Indian Achievers Forum.