

**SRIDEVI SATHYA PRIYA .S**

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**RESEARCH/TEACHING INTERESTS**

Design and implementation of high speed compact cryptosystems for military applications. Implementation of efficient Cryptographic system with iris and finger print biometrics as key to provide a secured environment to all confidential data. VLSI signal processing, Control systems, Digital system Design, Hardware description languages, CAD for VLSI Design. Microprocessor, Micro Controllers, SOC Design, ASIC Design

**EXPERIENCE**

Assistant Professor (AGP 8000), Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, India, 2011 - Present

Assistant Professor , Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, India, 2009- 2011

Lecturer, Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, Coimbatore, India, 2003-2007

**EDUCATION**

Degree	Branch / Specialization	University	Class	Mode	Month&Year of Passing
Ph.D.,	Electronics and Communication	Karunya Institute of Technology and Sciences	-	part Time	Feb 2017
M.E.,	VLSI Design	Karunya Institute of Technology and Sciences	First Class	Full Time	May 2006
B.E.,	Electronics and Communication	Madras University	First Class	Full Time	June 2001

**SUMMARY OF INTERNATIONAL JOURNAL& CONFERENCE PUBLICATIONS - [PUBLISHED & ACCEPTED]**

Year	International Journal Publications	International Conference Publications	National Conference publications
2007 - 2021	8	24	1

**PATENTS**

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**RESEARCH PROJECTS**

#	Title of the project	Funding Agency	Amount(Rs.)	Year

**RESEARCH GUIDANCE**

Research guidance as supervisor	Completed	Ongoing
Doctor of Philosophy - PhD Thesis advising		4
Master of Engineering - Master's Thesis advising	19	-

**PROFESSIONAL MEMBERSHIPS**

1	Life member of ISTE
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