

**MANOJ.G**

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**RESEARCH/TEACHING INTERESTS**

Analysis and design of Analog Integrated Circuits, VLSI Design and Optimization for medical applications, VLSI for IoT Systems, System-on-Chip (SoC) Design, Microprocessors, Microcontrollers, Linear Integrated Circuits, Electronic Devices, Low-Power Design Techniques, Internet of Things, Communication Engineering, VLSI Design, Digital Communication, Digital Electronics, Digital System Design, Analog Communication, Basic Electronics.

**EXPERIENCE**

- Assistant Professor (SG), Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, Coimbatore, India, 2019 – present.
- Assistant Professor, Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, India, 2010 – 2019.
- Lecture, Electronics and Communication Engineering, Kumaraguru College of Engineering and Technology, India, 2009 – 2010.
- Lecturer, Electronics and Communication Engineering, Park College of Engineering and Technology, Coimbatore, India, 2007 – 2009.
- Lecturer, Electronics and Communication Engineering, Erode Sengunthar Engineering College, Coimbatore, India, 2006 – 2007.

**EDUCATION**

Degree	Branch / Specialization	University	Class	Mode	Month&Year of Passing
Ph.D.	Electronics and Communication	Anna University	-	part Time	Feb 2016
M.E.	VLSI DESIGN	Karunya Deemed University	Distinction	Full Time	March 2006
B.E.	Electronics and Communication	Bharathiar University	First Class	Part Time	April 2003

**SUMMARY OF INTERNATIONAL JOURNAL& CONFERENCE PUBLICATIONS - [PUBLISHED & ACCEPTED]**

Year	International Journal Publications	International Conference Publications	National Conference publications
2009 - 2021	21	22	-

**PATENTS**

1	IOT based remote monitoring and alerting system using integrated dual-Communication system for human-animal conflict mitigation, Application No.202541069047 A, Publication Date: 19/07/2025.
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**RESEARCH PROJECTS**

#	Title of the project	Funding Agency	Amount(Rs.)	Year
1	Configurable and Scalable IOT SOC for Automobile Application	Ministry of Electronics and Information Technology	86.61 Lakhs	2023-2028
2.	SPICES Scheme-ECHO High Tech Organization	AICTEC	2 Lakhs	2022-2023
3.	Development of Electronic Speed Controller for Actuator with Feedback Mechanism", DRDO, Debel, Bangalore	DRDO	9.38 lacks	2019-2020
4.	Low Power Techniques for Biomedical Implantable Devices	Visvesvaraya Scheme for Electronics &IT	5Lakhs.	2019-2021
5.	Digitalization Technology in Rural India	MHRD	Rs.0.5Lakhs	2018-2019
6.	Human Animal Mitigation	MHRD	Rs 1Lakhs	2023-2024

**RESEARCH GUIDANCE**

Research guidance as supervisor	Completed	Ongoing
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Doctor of Philosophy - PhD Thesis advising	2	6
Master of Engineering - Master’s Thesis advising	25	-

**PROFESSIONAL MEMBERSHIPS**

1	Life member of ISTE
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**AWARDS/HONORS**

1. Rotaract Club of Karunya University Award Best Supportive Faculty laurels at Rotary International District 3206,2024-2025.

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