

**SAMSON IMMANUEL J**

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**RESEARCH/TEACHING INTERESTS**

Digital ASIC, Data Mining, Deep Learning, Signals and Systems, C++ and Data Structures, Linear Integrated Circuits, Electronic circuits, Digital System Design, VLSI Design, VLSI Signal Processing, Embedded Systems, VHDL, Verilog, Programming in C, Control systems, Statistical signal processing, FPGA, Low Power VLSI.

**EXPERIENCE**

Assistant Professor, Electronics and Communication Engineering, Karunya Institute of Technology and Sciences, Coimbatore, India, 2010 – present  
 Lecturer, Electronics and Communication Engineering, Park College of Engineering and Technology, India, 2006 – 2010

**EDUCATION**

Degree	Branch/ Specialization	University	Class	Mode	Month&Year of Passing
Ph.D.,	Electronics and Communication	Karunya University	-	Part Time	Pursuing
M.E.,	VLSI Design	Karunya University	First Class	Full Time	April 2006
B.E.,	Electrical and Electronics Engineering	Periyar University	Distinction	Full Time	April 2004

**SUMMARY OF INTERNATIONAL JOURNAL & CONFERENCE PUBLICATIONS - [PUBLISHED & ACCEPTED]**

Year	International Journal Publications	International Conference Publications	National Conference publications
2009 - 2021	11	9	3

**RESEARCH GUIDANCE**

Research guidance as supervisor	Completed	Ongoing
Master of Engineering - Master's Thesis advising	10	-

**PROFESSIONAL MEMBERSHIPS**

1	Life member of MISTE
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